

KONGUNADU ARTS AND SCIENCE COLLEGE (AUTONOMOUS), COIMBATORE - 29.

ACADEMIC PLANNING

Department: Computer Technology

Name of the Teacher: N. Senthil Kumar

Year 2022 - 2023

Title of the Paper: Digital Logic and

Subject Code: 22UCT202

Semester: ODD / EVEN

Test No. & Period	Topics allotted	No. of hours required for completion	Dates & hrs of handling	Topics not covered, if any	Signature of	
					Staff	HOD
I	<u>Unit-I:</u> Number System and Codes: Introduction - Number System - Floating Point Representation of Numbers - Arithmetic Operations - 1's and 2's Complements - 9's and 10's Complement - BCD-Codes. Logic Gates: Introduction - Logic Gates. <u>Unit-II:</u> Boolean Algebra and Minimization Techniques Introduction - Boolean logic operations - Basic laws of Boolean Algebra - De Morgan's Theorem - sum of Products. And Product of sum - K map. <u>Unit-III:</u> Arithmetic Circuits: Introduction - Procedure. Half Adder - Full Adder - Half Subtractor - Full Subtractor - Parallel Binary Adder - serial Adder	14 hours	02.01.2023 TO 02.02.2023 14 hours			
II		10 hours	08.02.2023 TO 21.02.2023 14 hours 22.02.2023 TO 14.03.2023			
To:						
From:						
To:						

Department : Computer Technology

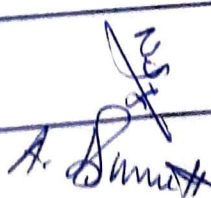
Name of the Teacher : A. J. Venkataratnam

Year 2022 - 20 23

Title of the Paper : Digital Logic And Circuit Design

Subject Code : 22000202

Semester : ODD / EVEN

Test No. & Period	Topics allotted	No. of hours required for completion	Dates & hrs of handling	Topics not covered, if any	Signature of	
					Staff	HOD
I From : To :	BCD Adder - Binary Multiplier - Binary Divider. <u>Unit - IV :</u> Combination Circuits : Multiplier - De - Multiplier - Decoder - Encoder - Flip - Flops ; Introduction - Latches - SR, D, JK, T Flip Flops Triggering of Flip Flops.	12 hours	18.3.2023 to 08.4.2023	—		
II From : To :	<u>Unit - V :</u> Counters : Introduction - Asynchronous Counter - Synchronous counters, Registers : Introduction - Shift Registers - Shift Counters.	12 hours	10.4.2023 to 29.4.2023	—		